



Dkt. 2271/70977

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Hitoshi YAMAMOTO

Serial No.: 10/656,434

Group Art Unit: 2826

Date Filed: September 5, 2003

Examiner: Alexander O. Williams

For: SEMICONDUCTOR DEVICE RESPONSIVE TO DIFFERENT LEVELS OF INPUT
AND OUTPUT SIGNALS AND SIGNAL PROCESSING SYSTEM USING THE
SAME (as amended)

I hereby certify that this correspondence is being deposited this date with
the U.S. Postal Service with sufficient postage as first class mail in an
envelope addressed to: Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450.

Paul Teng
Paul Teng
Reg. No. 40,837

January 17, 2005
Date

1185 Avenue of the Americas
New York, N.Y. 10036
(212) 278-0400

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

This Amendment is submitted in response to the Office Action dated September 16, 2004
in connection with the above-identified application.

Amendment to the Title begins on page 2 of this paper.

Amendments to the Claims are reflected in the **Listing of Claims** section which begins on
page 3.

Remarks begin on page 5 of this paper.

Amendment to the Title

Please amend the Title to the following:

SEMICONDUCTOR DEVICE ~~AND~~ RESPONSIVE TO DIFFERENT LEVELS OF
INPUT AND OUTPUT SIGNALS AND SIGNAL PROCESSING SYSTEMS
USING THE SAME